

VIDYA PRATISHTHAN'S COLLEGE OF ENGINEERING, BARAMATI

Department of Electronics & Telecommunication
Departmental Time Table 2025-26

SEM-II

(w e f - 15th Dec 2025)

Day	Time	9 00 To 10 00	10 00 To 11 00	11 00 To 11 15	11 15 To 12 15	12 15 To 01 15	1 15 To 2 30	02 30 To 03 30	03 30 To 04 30	04 30 To 04 45	04 45 To 05 45		
	Class/Period	1	2		3	4		5	6		7		
Monday	S Y	ADC -A1 (COMM LAB) BHP MC- A2(SP LAB) SRS CS-A4 (RMT LAB) VSS/VUD MDM OOP-A3 (VLSI LAB) RHA		S H O R T R E C E S S	ADC- BHP (15)	MC-SRS (15)	L u n c h	CS - VSS/VUD (15)	MDM- RHA (15)	S H O R T R E C E S S	OE-15		
	T Y	ML-PNA (14)	MCN-MMJ (14)		MCN-A1 (COMM LAB) MMJ ML-A2 (VLSI LAB) PNA IoT-A3 (SSD LAB) NSB VSC-A4 (SP LAB) - PG			IoT- NSB (14)	TY AC-Conf Hall		OE-Conf Hall		
	B E					ESD-JSR (15)		I&E-A2 (SSD LAB) MMJ FOC-A3 (RMT Lab) RSP DBM-A1 (SP Lab) SRS	FOC-RSP (14)				
Tuesday	S Y	ADC -A2 (COMM LAB) BHP MC- A3(SP LAB) SRS CS-A1(RMT LAB) VSS/VUD MDM OOP-A4 (VLSI LAB) RHA				ADC- BHP (15)	MC-SRS (15)	L u n c h	CS - VSS/VUD (15)		Library slot		OE-15
	T Y	ML-PNA (14)	IoT- NSB (14)		MCN-A2 (COMM LAB) MMJ ML-A3 (VLSI LAB) PNA IoT-A4 (SSD LAB) NSB VSC-A1 (SP LAB) - JSR		ES-RJP(14)		MDM-T1 (14)		OE-Conf Hall		
	B E					RS-RKS (15)	ESD-A1 (VLSI Lab) JSR I&E-A3 (SP LAB) MMJ FOC-A2 (RMT Lab) RSP		FOC-RSP (14)				
Wednesday	S Y	ADC -A3 (COMM LAB) BHP MC- A4(SP LAB) SRS MDM OOP-A1 (VLSI LAB) RHA CS -A2 (RMT LAB)-VSS/VUD				ADC- BHP (15)	CS - VSS/VUD (15)	L u n c h	AEC Course-ABP-(15)		ES-RJP(15)		AEC Course-RSP-(15)
	T Y	MCN-MMJ (14)	IoT- NSB (14)		MCN-A3 (COMM LAB) MMJ ML-A4 (VLSI LAB) PNA IoT-A1 (SSD LAB) NSB		VSC-A2 (COMM LAB) - PNA VSC-A1 (SP LAB) - PG						
	B E					ESD-JSR (15)	ESD-A3 (VLSI Lab) JSR FOC-A1 (RMT Lab) RSP DBM-A2 (SSD Lab) SRS						
Thursday	S Y	ADC -A4 (COMM LAB) BHP CS-A3 (RMT LAB) VSS/VUD MC-A1(SP LAB) JSR MDM OOP-A2 (VLSI LAB) RHA				MC-SRS (15)	MDM- RHA (15)	L u n c h	AEC Lab-A2-LANG Lab-NB-(ABP) AEC Lab-A4-VLSI Lab-(RSP)				
	T Y	MCN-MMJ (14)	ML-PNA (14)		MCN-A4 (COMM LAB) MMJ ML-A1 (VLSI LAB) PNA IoT-A2 (SSD LAB) NSB VSC-A3 (SP LAB) - BHP		VSC-A2 (COMM LAB) - PNA VSC-A3 (SP LAB) - BHP VSC-A4 (SSD LAB) - PG		Library slot				
	B E					FOC-RSP (14)	RS-RKS(15)		RS-RKS (15)				
Friday	S Y	ES-RJP(15)	MDM- RHA (15)		AEC Lab-A1-LANG Lab-NB-(ABP) AEC Lab-A3-VLSILab-(RSP)		L u n c h	Honor SY VLSI-SP Lab-VSS			Honor VLSI-VSS(15)		
	T Y	MDM TY Lab (VLSI Lab)		ES-RJP(14)	MDM-T1 (14)								
	B E				ESD-JSR (15)	DBM-A3 (SSD Lab) SRS I&E-A1 (RMT LAB) MMJ ESD-A2 (VLSI Lab) JSR							
Saturday	S Y												
	T Y	Honor VLSI(14)			Honor VLSI Lab(VLSI Lab)								

SY

ADC -Analog and Digital Communication- Dr B H Pati

CS Control Systems - V S Surwase

MC - Microcontrollers -S R Sabale/Dr J S Rangole

OOP - Multidisciplinary Minor Course-Object Oriented Programming- Mr R H Ambole

OE - Open Elective (Digital Marketing)-T1

ES -Environmental Studies-Dr R J Patil

AEC -Public Speaking and Aptitude-A B Patil/R S Piske

Honor VLSI - V S Surwase

TY

MCN- Mobile Communication and Networks -Dr Prabir Ghosh

ML - Machine Learning - Dr P N Arotale

IoT - Internet of Things -N S Bradar

MDM -Multidisciplinary Minor Course - T5

VSC -Vocational and Skill Enhancement Course (VSC)- Dr B H Pati/V S Surwase/Dr P N Arotale/Dr J S Rangole

OE - Open Elective (Intellectual Property Rights)-T6

ES -Environmental Studies-Dr R J Patil

AC -Audit Course-Democracy Election and Governance-T2

Honor VLSI - T3

BE

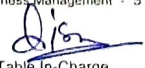
FOC Fiber Optic Communication - R S Piske

Elec V (ESD) Embedded System design- Dr J S Rangole

Elec VI (RS) - Remote Sensing - Dr R K Shastri

I &E -Innovation & Entrepreneurship - Dr M M Jadhav

DBM -Digital Business Management - S R Sabale


Time-Table In-Charge
R S Piske


Time-Table Co-ordinator
Dr. S. K. Shinde


Dean Academics
Dr S M Bhosale


(H.O.D.)
Dr. J. S. Rangole


Principal
Principals

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